EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	806	feedback adj clock	USPAT	OR	OFF	2007/01/31 11:16
S2	8	feedback adj clock with processor	USPAT	OR	OFF	2007/01/31 11:15
S3	9	feedback adj clock with processor	US-PGPUB	OR	OFF	2007/01/31 11:15
S4	0	feedback adj clock with processor	EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:16
S5	0	feedback adj clock with cpu	USPAT	OR	OFF	2007/01/31 11:16
S6	1	feedback adj clock with power$1on	USPAT	OR	OFF	2007/01/31 11:17
S7	1	feedback adj clock with power$1on	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:17
S8	63	feedback adj clock with reset	USPAT	OR	OFF	2007/01/31 11:19
S9	0	feedback adj clock with reset and over$1clock$3	USPAT	OR	OFF	2007/01/31 11:18
S10	2	feedback adj clock with reset same (processor cpu)	USPAT	OR	OFF	2007/01/31 11:19
S11	2	feedback adj clock with reset same (processor cpu)	US-PGPUB	OR	OFF	2007/01/31 11:20
S12	0	feedback adj clock with reset same (processor cpu)	EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:20
S13	13	over$1clock$3 near3 (prevent$3 detect$3)	USPAT	OR	OFF	2007/01/31 11:22
S14	16	over$1clock$3 near3 (prevent$3 detect$3)	US-PGPUB	OR	OFF	2007/01/31 11:23
S15	0	over$1clock$3 near3 (prevent$3 detect$3)	EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:25
S16	1	processor with clock adj rate adj setting	USPAT	OR	OFF	2007/01/31 11:27
S17	1	processor with clock adj rate adj setting	US-PGPUB; EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:27
S18	14	provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2007/01/31 11:29
S19	3	provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	US-PGPUB	OR	OFF	2007/01/31 11:33
S20	1	provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	EPO; JPO; IBM_TDB	OR	OFF	2007/01/31 11:33
S21	8	kurts.in. with tsvika	USPAT	OR	OFF	2007/01/31 11:38
S22	14	horigan.in. with john	USPAT	OR	OFF	2007/01/31 11:39
S23	826	713/600.ccls.	USPAT	OR	OFF	2007/01/31 11:44
S24	0	713/600.ccls. and feedback adj clock with processor	USPAT	OR	OFF	2007/01/31 11:55

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S25	0	713/600.ccls. and feedback adj clock with reset same (processor cpu)	USPAT	OR	OFF	2007/01/31 11:56
S26	1	713/600.ccls. and over$1clock$3 near3 (prevent$3 detect$3)	USPAT	OR	OFF	2007/01/31 11:57
S27	0	713/600.ccls. and processor with clock adj rate adj setting	USPAT	OR	OFF	2007/01/31 11:57
S28	1	713/600.ccls. and provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2007/01/31 11:57
S29	733	713/501.ccls.	USPAT	OR	OFF	2007/01/31 12:04
S30	1	713/501.ccls. and feedback adj clock with processor	USPAT	OR	OFF	2007/01/31 12:12
S31	1	713/600.ccls. and over$1clock$3 near3 (prevent$3 detect$3)	USPAT	OR	OFF	2007/01/31 12:12
S32	4	713/501.ccls. and over$1clock$3 near3 (prevent$3 detect$3)	USPAT	OR	OFF	2007/01/31 12:13
S33	0	713/501.ccls. and processor with clock adj rate adj setting	USPAT	OR	OFF	2007/01/31 12:16
S34	0	713/501.ccls. and feedback adj clock with reset same (processor cpu)	USPAT	OR	OFF	2007/01/31 12:16
S35	3	713/501.ccls. and provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2007/01/31 12:16
S36	369	327/292.ccls.	USPAT	OR	OFF	2007/01/31 12:22
S37	0	327/292.ccls. and feedback adj clock with processor	USPAT	OR	OFF	2007/01/31 12:26
S38	1	327/292.ccls. and over$1clock$3 near3 (prevent$3 detect$3)	USPAT	OR	OFF	2007/01/31 12:26
S39	0	327/292.ccls. and processor with clock adj rate adj setting	USPAT	OR	OFF	2007/01/31 12:27
S40	0	327/292.ccls. and feedback adj clock with reset same (processor cpu)	USPAT	OR	OFF	2007/01/31 12:27
S41	0	327/292.ccls. and provid$3 near3 (clock near2 (speed rate)) near3 (cpu processor)	USPAT	OR	OFF	2007/01/31 12:28